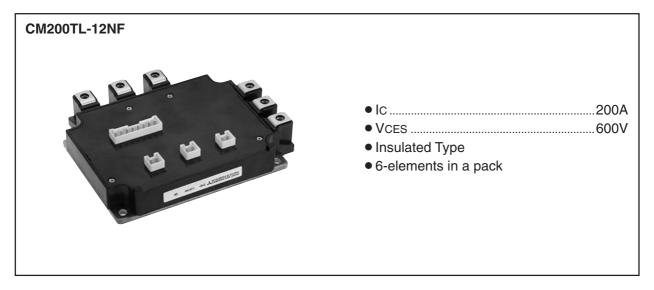
MITSUBISHI IGBT MODULES

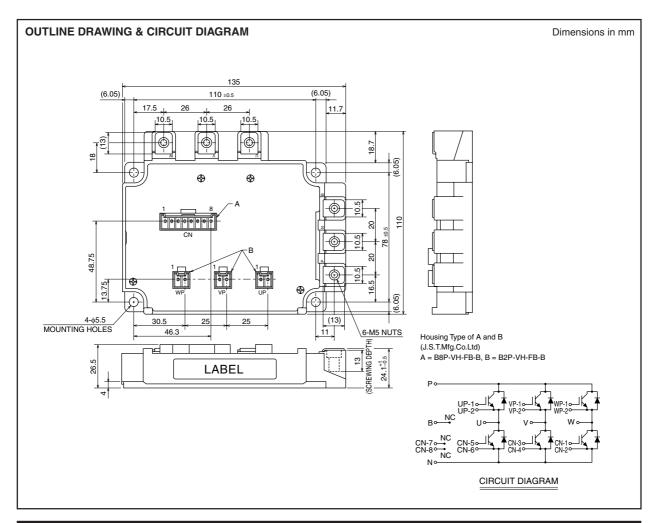
# **CM200TL-12NF**

HIGH POWER SWITCHING USE



#### **APPLICATION**

AC drive inverters & Servo controls, etc





### **HIGH POWER SWITCHING USE**

#### ABSOLUTE MAXIMUM RATINGS (Tj = 25°C, unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit	
VCES	Collector-emitter voltage	G-E Short		600	V
VGES	Gate-emitter voltage	C-E Short		±20	V
Ic	Collector current	DC, $Tc = 88^{\circ}C^{*1}$		200	Α
Ісм	Collector current	ulse (Note 2)		400	Α
IE (Note 1)	Emitter current			200	Α
IEM (Note 1)	Emiller current	Pulse	(Note 2)	400	Α
PC (Note 3)	Maximum collector dissipation	Tc = 25°C		890	W
Tj	Junction temperature			<b>−</b> 40 ~ +150	°C
Tstg	Storage temperature			<b>−</b> 40 ~ +125	°C
Viso	Isolation voltage	Terminals to base plate, f = 60Hz, AC 1 minu	ıte	2500	Vrms
_	Torque etropath	Main terminals M5 screw		2.5 ~ 3.5	N•m
_	Torque strength	Mounting M5 screw		2.5 ~ 3.5	N•m
_	Weight	Typical value		750	g

#### ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified)

Symbol	Parameter	Test conditions		Limits			1.1
				Min.	Тур.	Max.	Unit
ICES	Collector cutoff current	VCE = VCES, VGE = 0V		_	_	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 20mA, VCE = 10V		6	7	8	V
IGES	Gate leakage current	±VGE = VGES, VCE = 0V		_	_	0.5	μΑ
VCE(sat)	Collector-emitter saturation voltage	IC = 200A, VGE = 15V	Tj = 25°C	_	1.7	2.2	V
			Tj = 125°C	_	1.7	_	
Cies	Input capacitance	VCE = 10V VGE = 0V		_	_	30	nF
Coes	Output capacitance			_	_	3.7	nF
Cres	Reverse transfer capacitance			_	_	1.2	nF
QG	Total gate charge	VCC = 300V, IC = 200A, VGE = 15V		_	800	_	nC
td(on)	Turn-on delay time	Vcc = 300V, Ic = 200A $V\text{GE} = \pm 15V$ $R\text{G} = 3.1\Omega, \text{ Inductive load}$ $\text{IE} = 200A$		_	_	120	ns
tr	Turn-on rise time			_	_	100	ns
td(off)	Turn-off delay time			_	_	300	ns
tf	Turn-off fall time			_	_	300	ns
trr (Note 1)	Reverse recovery time			_	_	150	ns
Qrr (Note 1)	Reverse recovery charge			_	4.8	_	μС
VEC(Note 1)	Emitter-collector voltage	IE = 200A, VGE = 0V			_	2.8	V
Rth(j-c)Q	The word variations	IGBT part (1/6 module)*1		_	_	0.14	K/W
Rth(j-c)R	Thermal resistance	FWDi part (1/6 module)*1		_	_	0.22	K/W
Rth(c-f)	Contact thermal resistance	Case to heat sink, Thermal compound Applied (1/6 module)*2			0.051	_	K/W
Rg	External gate resistance			3.1	_	31	Ω



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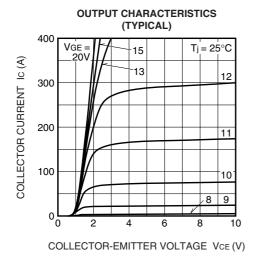
<sup>\*1 :</sup> Case temperature (Tc) measured point is just under the chips.
If you use this value, Rth(f-a) should be measured just under the chips.
\*2 : Typical value is measured by using thermally conductive grease of λ = 0.9[W/(m • K)].

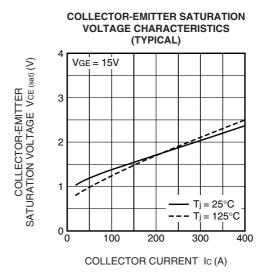
Note 1. IE, VEC, trr & Qrr represent characteristics of the anti-parallel, emitter-collector free-wheel diode (FWDi).

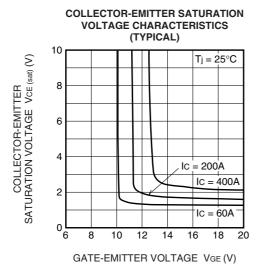
Pulse width and repetition rate should be such that the device junction temperature (Tj) does not exceed T<sub>jmax</sub> rating.
 Junction temperature (Tj) should not increase beyond 150°C.
 Pulse width and repetition rate should be such as to cause negligible temperature rise.

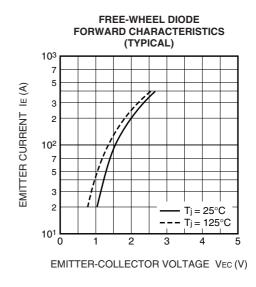
#### HIGH POWER SWITCHING USE

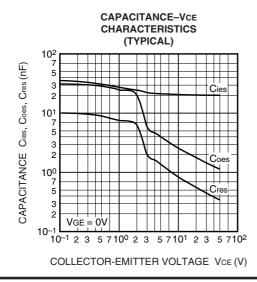
#### **PERFORMANCE CURVES**

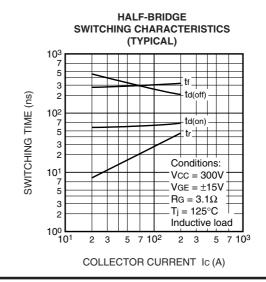










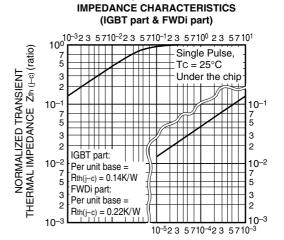




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#### HIGH POWER SWITCHING USE

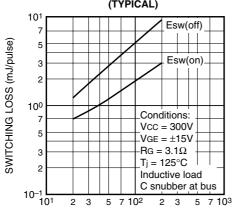
#### REVERSE RECOVERY CHARACTERISTICS OF FREE-WHEEL DIODE (TYPICAL) REVERSE RECOVERY CURRENT In (A) 10<sup>3</sup> REVERSE RECOVERY TIME trr (ns) 7 5 3 2 10<sup>2</sup> trr Conditions: 5 Vcc = 300V $VGE = \pm 15V$ 3 $RG = 3.1\Omega$ 2 Tj = 25°C 101 L 101 Inductive load 2 3 5 7 10<sup>2</sup>



TRANSIENT THERMAL

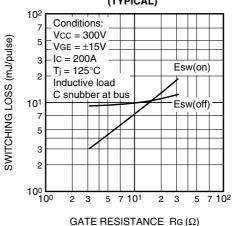
SWITCHING LOSS vs. **COLLECTOR CURRENT** (TYPICAL) 101

EMITTER CURRENT IE (A)

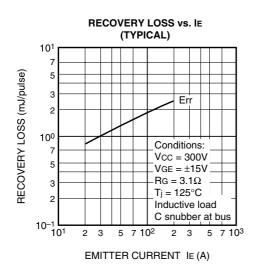




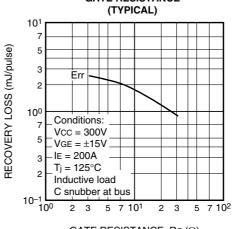
TIME (s)



COLLECTOR CURRENT Ic (A)



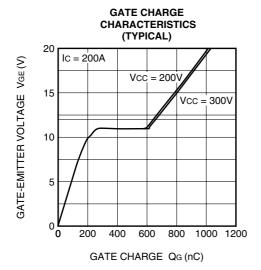
**RECOVERY LOSS vs. GATE RESISTANCE** 



GATE RESISTANCE RG  $(\Omega)$ 



### **HIGH POWER SWITCHING USE**





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