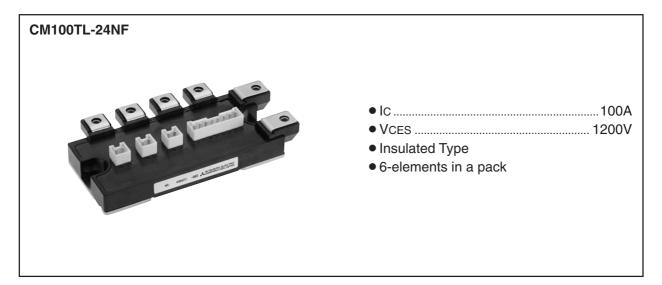
MITSUBISHI IGBT MODULES

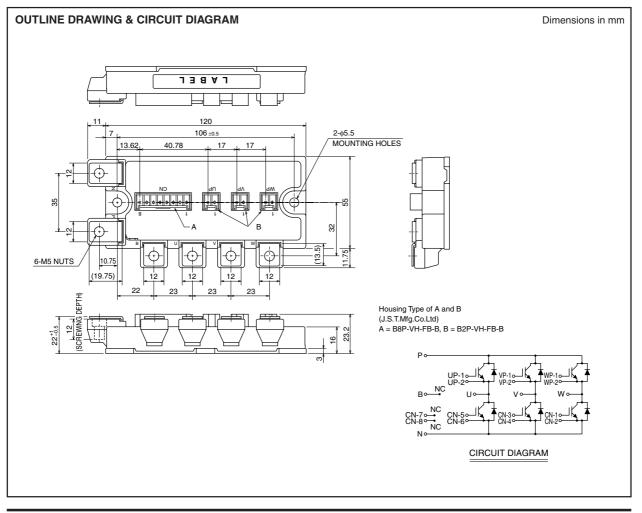
CM100TL-24NF

HIGH POWER SWITCHING USE



APPLICATION

AC drive inverters & Servo controls, etc





HIGH POWER SWITCHING USE

ABSOLUTE MAXIMUM RATINGS (Tj = 25°C, unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit	
VCES	Collector-emitter voltage	G-E Short		1200	V
VGES	Gate-emitter voltage	C-E Short		±20	V
IC	Collector current	DC, TC = $80^{\circ}C^{*1}$	100	A	
Ісм	Collector current	Pulse	(Note 2)	200	A
IE (Note 1)				100	A
IEM (Note 1)	Emitter current	Pulse	(Note 2)	200	A
PC (Note 3)	Maximum collector dissipation	$Tc = 25^{\circ}C$		620	W
Tj	Junction temperature			-40 ~ +150	°C
Tstg	Storage temperature			-40 ~ +125	°C
Viso	Isolation voltage	Terminals to base plate, f = 60Hz, AC	1 minute	2500	Vrms
_	To you to other other	Main terminals M5 screw		2.5 ~ 3.5	N • m
_	Torque strength	Mounting M5 screw		2.5 ~ 3.5	N • m
	Weight	Typical value		350	g

ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified)

Symbol	Demonster	Test conditions		Limits			
	Parameter			Min.	Тур.	Max.	Unit
ICES	Collector cutoff current	VCE = VCES, VGE = 0V		—	—	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 10mA, VCE = 10V		6	7	8	v
IGES	Gate leakage current	$\pm VGE = VGES, VCE = 0V$		_	_	0.5	μA
VCE(sat)	Collector-emitter saturation voltage	IC = 100A, VGE = 15V	$T_j = 25^{\circ}C$	_	2.1	3.0	- V
			Tj = 125°C	—	2.4	—	
Cies	Input capacitance	VCE = 10V VGE = 0V		—	—	17.5	nF
Coes	Output capacitance			—	_	1.5	nF
Cres	Reverse transfer capacitance			_	_	0.34	nF
QG	Total gate charge	VCC = 600V, IC = 100A, VGE = 15V		_	500	_	nC
td(on)	Turn-on delay time			_	_	100	ns
tr	Turn-on rise time	Vcc = 600V, Ic = 100A VGE = $\pm 15V$ RG = 3.1Ω , Inductive load IE = 100A		_	_	70	ns
td(off)	Turn-off delay time			_	_	300	ns
tf	Turn-off fall time			_	_	350	ns
trr (Note 1)	Reverse recovery time			_	_	150	ns
Qrr (Note 1)	Reverse recovery charge			_	4.8	_	μC
VEC(Note 1)	Emitter-collector voltage	IE = 100A, VGE = 0V		_	_	3.8	V
Rth(j-c)Q		IGBT part (1/6 module) ^{*1}		_	_	0.20	K/W
Rth(j-c)R	Thermal resistance	FWDi part (1/6 module) ^{*1}		_	_	0.28	K/W
Rth(c-f)	Contact thermal resistance	Case to heat sink, Thermal compound Applied (1/6 module)*2			0.085	_	K/W
RG	External gate resistance			3.1	_	42	Ω

*1 : Case temperature (Tc) measured point is just under the chips. If you use this value, Rth(f-a) should be measured just under the chips.
*2 : Typical value is measured by using thermally conductive grease of λ = 0.9[W/(m • K)].

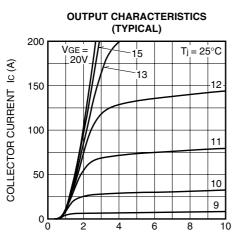
Note 1. IE, VEC, trr & Qrr represent characteristics of the anti-parallel, emitter-collector free-wheel diode (FWDi).

Pulse width and repetition rate should be such that the device junction temperature (Tj) does not exceed Tjmax rating.
Junction temperature (Tj) should not increase beyond 150°C.
Pulse width and repetition rate should be such as to cause negligible temperature rise.



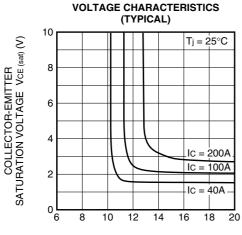
HIGH POWER SWITCHING USE



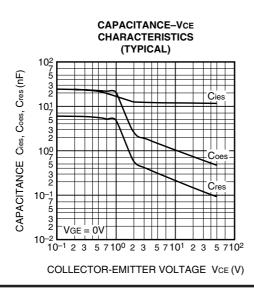


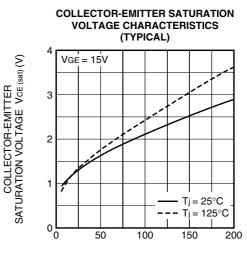
COLLECTOR-EMITTER VOLTAGE VCE (V)

COLLECTOR-EMITTER SATURATION



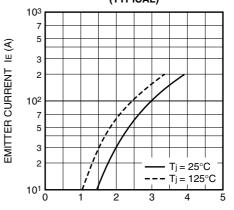
GATE-EMITTER VOLTAGE VGE (V)





COLLECTOR CURRENT Ic (A)

FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



EMITTER-COLLECTOR VOLTAGE VEC (V)

HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL) 10³ td(off) 5 tf 3 2 SWITCHING TIME (ns) 10² 5 td(on) 3 tr 2 Conditions: 10¹ VCC = 600VVGE = ±15V 5 RG = 3.1Ω 3 Ti = 125°C 2 Inductive load 100 L 101 2 3 5 7 10² 2 3 5 7 10³ COLLECTOR CURRENT Ic (A)



HIGH POWER SWITCHING USE

- Single Pulse,

Under the chip

 10^{-1}

7 5

3

2

; 5

3

2

110-3

Esw(on)

Esw(off)

5 7 10²

2 3

Conditions:

VCC = 600V

IE = 100A

Tj = 125°C

Inductive load

2 3

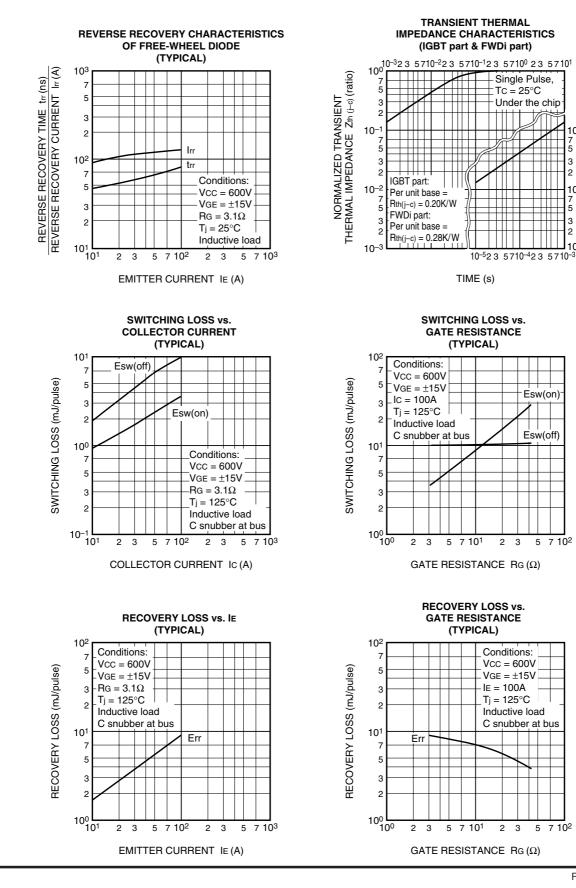
5 7 10²

C snubber at bus

 $VGE = \pm 15V$

10-2

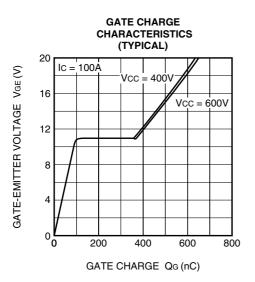
 $Tc = 25^{\circ}C$







HIGH POWER SWITCHING USE





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