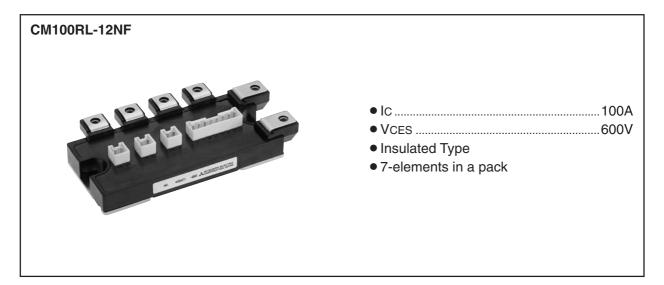
MITSUBISHI IGBT MODULES

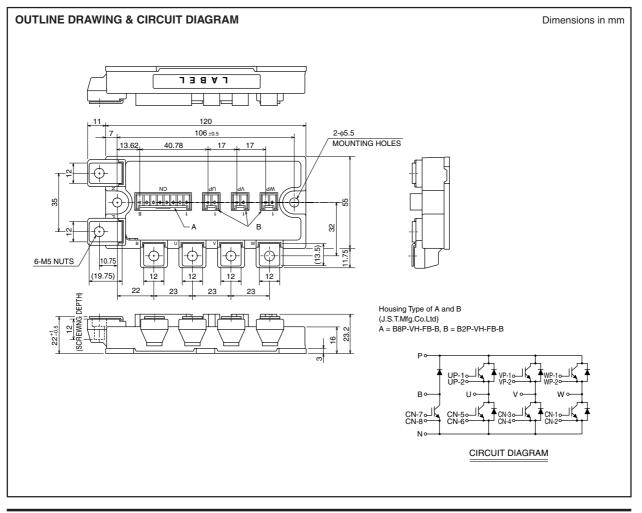
CM100RL-12NF

HIGH POWER SWITCHING USE



APPLICATION

AC drive inverters & Servo controls, etc





HIGH POWER SWITCHING USE

ABSOLUTE MAXIMUM RATINGS (Tj = 25° C, unless otherwise specified) INVERTER PART

Symbol	Parameter	Conditions	;	Ratings	Unit
VCES	Collector-emitter voltage	G-E Short		600	V
VGES	Gate-emitter voltage	C-E Short		±20	V
IC		DC, Tc = 99°C ^{*1}		100	Α
Ісм	Collector current	Pulse	(Note 2)	200	Α
IE (Note 1)	Emitter current			100	Α
IEM (Note 1)	Emiller current	Pulse	(Note 2)	200	Α
PC (Note 3)	Maximum collector dissipation	Tc = 25°C		540	W

BRAKE PART

Symbol	Parameter	Conditions		Ratings	Unit
VCES	Collector-emitter voltage	G-E Short		600	V
VGES	Gate-emitter voltage	C-E Short		±20	V
IC	Collector current	DC, Tc = 107°C ^{*1}		50	Α
Ісм	Collector current	Pulse	(Note 2)	100	Α
PC (Note 3)	Maximum collector dissipation	$Tc = 25^{\circ}C$		320	W
VRRM	Repetitive peak reverse voltage	Clamp diode part		600	V
IFM	Forward current	Clamp diode part		50	Α

(COMMON RATING)

Symbol	Parameter	Conditions	Ratings	Unit
Tj	Junction temperature		-40 ~ +150	°C
Tstg	Storage temperature		-40 ~ +125	°C
Viso	Isolation voltage	Terminals to base plate, f = 60Hz, AC 1 minute	2500	Vrms
_	Torque strength	Main terminals M5 screw	2.5 ~ 3.5	N•m
_		Mounting M5 screw	2.5 ~ 3.5	N•m
_	Weight	Typical value	350	g



HIGH POWER SWITCHING USE

ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified) **INVERTER PART**

Oursels al	Devenuenten	Test conditions		Limits			1.124
Symbol	Parameter			Min.	Тур.	Max.	Unit
ICES	Collector cutoff current	VCE = VCES, VGE = 0V		—	_	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 10mA, VCE = 10V		6	7	8	V
IGES	Gate leakage current	\pm VGE = VGES, VCE = 0V		_	_	0.5	μA
		$T_j = 25^{\circ}C$	—	1.7	2.2		
VCE(sat)	Collector-emitter saturation voltage	IC = 100A, VGE = 15V	Tj = 125°C	—	1.7	_	V
Cies	Input capacitance	VCE = 10V VGE = 0V		—	_	15	nF
Coes	Output capacitance			—	_	1.9	nF
Cres	Reverse transfer capacitance			—	_	0.6	nF
QG	Total gate charge	Vcc = 300V, Ic = 100A, Vge = 15V		—	400	—	nC
td(on)	Turn-on delay time	Vcc = 300V, lc = 100A VGE = $\pm 15V$ RG = 6.3Ω , Inductive load IE = 100A		_	_	120	ns
tr	Turn-on rise time			_	_	100	ns
td(off)	Turn-off delay time			_	_	300	ns
tŕ	Turn-off fall time			_	_	300	ns
trr (Note 1)	Reverse recovery time			_	_	120	ns
Qrr (Note 1)	Reverse recovery charge			_	2.1	—	μC
VEC(Note 1)	Emitter-collector voltage	IE = 100A, VGE = 0V		_	_	2.8	V
Rth(j-c)Q	The sum of the sister of a	Thermal resistance IGBT part (1/6 module) ^{*1} FWDi part (1/6 module) ^{*1}		_	_	0.23	K/W
Rth(j-c)R	I nermai resistance			_	_	0.41	K/W
Rth(c-f)	Contact thermal resistance	Case to heat sink, Thermal compound Applied (1/6 module)*2		_	0.085	_	K/W
RG	External gate resistance			6.3	_	63	Ω

BRAKE PART

0 1 1	Parameter	Test conditions			Limits		
Symbol				Min.	Тур.	Max.	Unit
ICES	Collector cutoff current	VCE = VCES, VGE = 0V			—	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 5.0mA		6	7	8	V
IGES	Gate leakage current	$\pm VGE = VGES, VCE = 0V$		_	_	0.5	μA
VCE(sat)	Collector-emitter saturation voltage	IC = 50A, VGE = 15V	Tj = 25°C		1.7	2.2	- V
			Tj = 125°C	_	1.7	—	
Cies	Input capacitance	N/ 10//	1		_	7.5	nF
Coes	Output capacitance	VCE = 10V		_	_	1.0	nF
Cres	Reverse transfer capacitance	VGE = 0V			_	0.3	nF
QG	Total gate charge	Vcc = 300V, Ic = 50A, VGE = 15V		_	200	-	nC
VFM	Forward voltage drop	IF = 50A		_	_	2.8	V
Rth(j-c)Q	Thermel registeres	IGBT part ^{*1}		_	_	0.39	K/W
Rth(j-c)R	Thermal resistance	Clamp diode part ^{*1}		_	_	0.70	K/W
Rg	External gate resistance			13	_	130	Ω

*1 : Case temperature (Tc) measured point is just under the chips. If you use this value, Rth(f-a) should be measured just under the chips.
*2 : Typical value is measured by using thermally conductive grease of λ = 0.9[W/(m • K)].

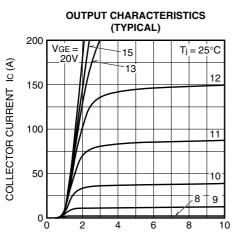
Note 1. IE, VEC, trr & Qrr represent characteristics of the anti-parallel, emitter-collector free-wheel diode (FWDi).

Pulse width and repetition rate should be such that the device junction temperature (Tj) does not exceed Tjmax rating.
Junction temperature (Tj) should not increase beyond 150°C.
Pulse width and repetition rate should be such as to cause negligible temperature rise.



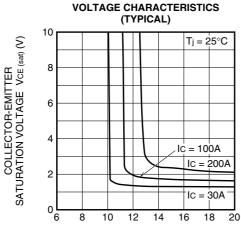
HIGH POWER SWITCHING USE





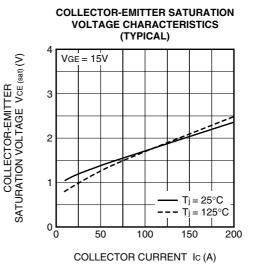
COLLECTOR-EMITTER VOLTAGE VCE (V)

COLLECTOR-EMITTER SATURATION

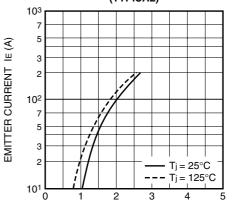


GATE-EMITTER VOLTAGE VGE (V)

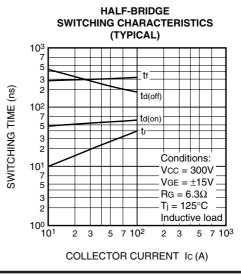
CAPACITANCE-VCE **CHARACTERISTICS** (TYPICAL) 10² 7 5 Cies, Coes, Cres (nF) 3 2 Cies 101 5 3 CAPACITANCE 2 100 7 5 3 2 VGE = 0V10-1 10-1 2 3 5 7 100 2 3 5 7 101 2 3 5 7 102 COLLECTOR-EMITTER VOLTAGE VCE (V)



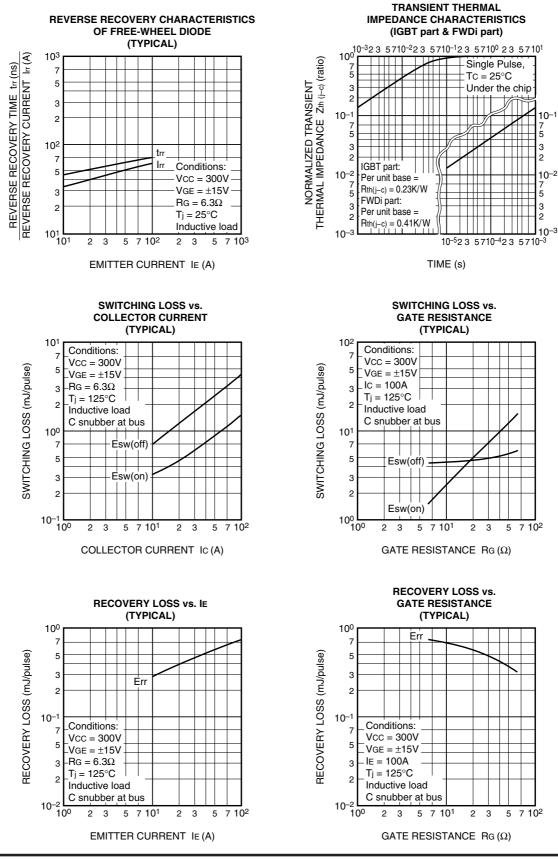
FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



EMITTER-COLLECTOR VOLTAGE VEC (V)

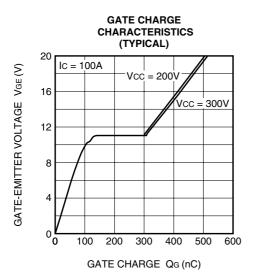


HIGH POWER SWITCHING USE





HIGH POWER SWITCHING USE





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