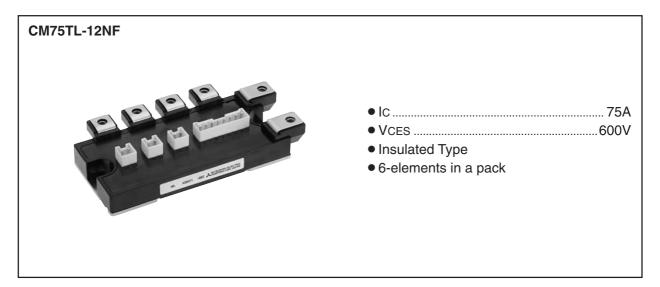
MITSUBISHI IGBT MODULES

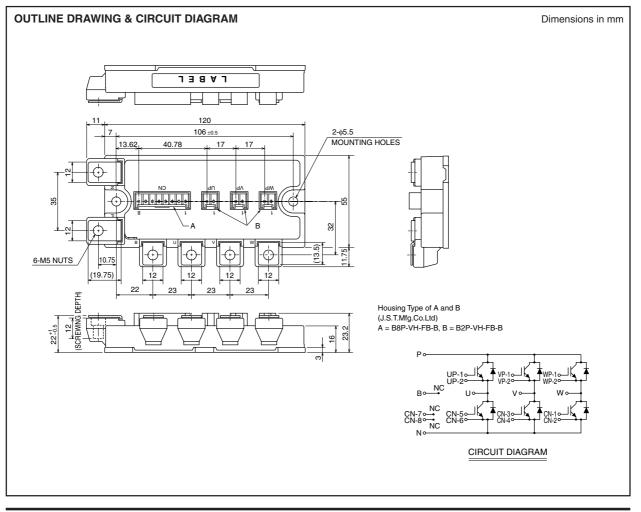
CM75TL-12NF

HIGH POWER SWITCHING USE



APPLICATION

AC drive inverters & Servo controls, etc





HIGH POWER SWITCHING USE

ABSOLUTE MAXIMUM RATINGS (Tj = 25°C, unless otherwise specified)

| Symbol | Parameter | Conditions | Ratings | Unit | |
|--------------|-------------------------------|---|----------|------------|-------|
| VCES | Collector-emitter voltage | G-E Short | | 600 | V |
| VGES | Gate-emitter voltage | C-E Short | | ±20 | V |
| Ic | Collector current | DC, Tc = $102^{\circ}C^{*1}$ | | 75 | A |
| Ісм | Collector current | Pulse | (Note 2) | 150 | A |
| IE (Note 1) | Emitter current | | | 75 | A |
| IEM (Note 1) | Emiller current | Pulse | (Note 2) | 150 | A |
| PC (Note 3) | Maximum collector dissipation | $Tc = 25^{\circ}C$ | | 430 | W |
| Tj | Junction temperature | | | -40 ~ +150 | °C |
| Tstg | Storage temperature | | | -40 ~ +125 | °C |
| Viso | Isolation voltage | Terminals to base plate, f = 60Hz, AC 1 | minute | 2500 | Vrms |
| _ | To you to a two parts | Main terminals M5 screw | | 2.5 ~ 3.5 | N • m |
| | Torque strength | Mounting M5 screw | | 2.5 ~ 3.5 | N • m |
| _ | Weight | Typical value | | 350 | g |

ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified)

| Symbol | Demonster | Test conditions | | Limits | | | |
|--------------|--------------------------------------|--|------------|--------|-------|------|------|
| | Parameter | | | Min. | Тур. | Max. | Unit |
| ICES | Collector cutoff current | VCE = VCES, VGE = 0V | | — | — | 1 | mA |
| VGE(th) | Gate-emitter threshold voltage | IC = 7.5mA, VCE = 10V | | 6 | 7 | 8 | v |
| IGES | Gate leakage current | $\pm VGE = VGES, VCE = 0V$ | | _ | _ | 0.5 | μA |
| VCE(sat) | Collector-emitter saturation voltage | IC = 75A, VGE = 15V | Tj = 25°C | _ | 1.7 | 2.2 | - V |
| | | | Tj = 125°C | — | 1.7 | _ | |
| Cies | Input capacitance | VCE = 10V VGE = 0V | | _ | — | 11.3 | nF |
| Coes | Output capacitance | | | — | — | 1.4 | nF |
| Cres | Reverse transfer capacitance | | | _ | _ | 0.45 | nF |
| QG | Total gate charge | VCC = 300V, IC = 75A, VGE = 15V | | — | 300 | — | nC |
| td(on) | Turn-on delay time | | | _ | — | 120 | ns |
| tr | Turn-on rise time | Vcc = 300V, Ic = 75A VGE = $\pm 15V$ RG = 8.3Ω , Inductive load IE = 75A | | _ | _ | 100 | ns |
| td(off) | Turn-off delay time | | | _ | — | 300 | ns |
| tf | Turn-off fall time | | | _ | _ | 300 | ns |
| trr (Note 1) | Reverse recovery time | | | _ | _ | 100 | ns |
| Qrr (Note 1) | Reverse recovery charge | | | _ | 1.2 | _ | μC |
| VEC(Note 1) | Emitter-collector voltage | IE = 75A, VGE = 0V | | _ | _ | 2.8 | V |
| Rth(j-c)Q | The sum of the sistence of | IGBT part (1/6 module) ^{*1} | | _ | _ | 0.29 | K/W |
| Rth(j-c)R | Thermal resistance | FWDi part (1/6 module) ^{*1} | | _ | _ | 0.51 | K/W |
| Rth(c-f) | Contact thermal resistance | Case to heat sink, Thermal compound Applied (1/6 module)*2 | | _ | 0.085 | _ | K/W |
| RG | External gate resistance | | | 8.3 | _ | 83 | Ω |

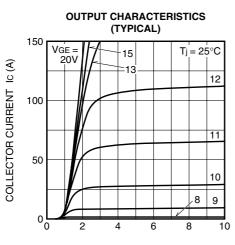
*1 : Case temperature (Tc) measured point is just under the chips. If you use this value, Rth(f-a) should be measured just under the chips.
 *2 : Typical value is measured by using thermally conductive grease of λ = 0.9[W/(m • K)].

Note 1. IE, VEC, trr & Qrr represent characteristics of the anti-parallel, emitter-collector free-wheel diode (FWDi).
2. Pulse width and repetition rate should be such that the device junction temperature (Tj) does not exceed Tjmax rating.
3. Junction temperature (Tj) should not increase beyond 150°C.
4. Pulse width and repetition rate should be such as to cause negligible temperature rise.



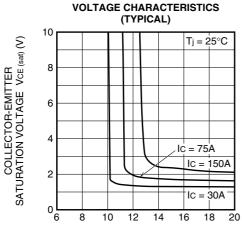
HIGH POWER SWITCHING USE



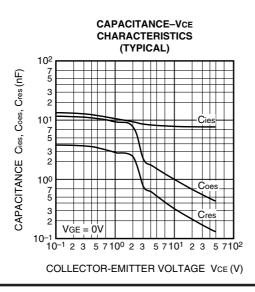


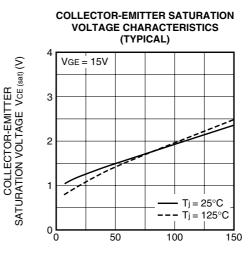
COLLECTOR-EMITTER VOLTAGE VCE (V)

COLLECTOR-EMITTER SATURATION



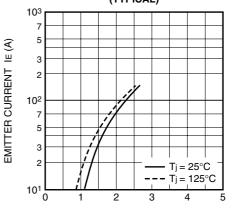
GATE-EMITTER VOLTAGE VGE (V)



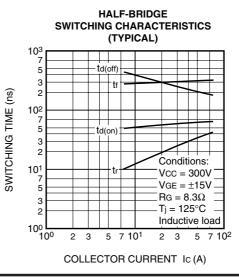


COLLECTOR CURRENT Ic (A)

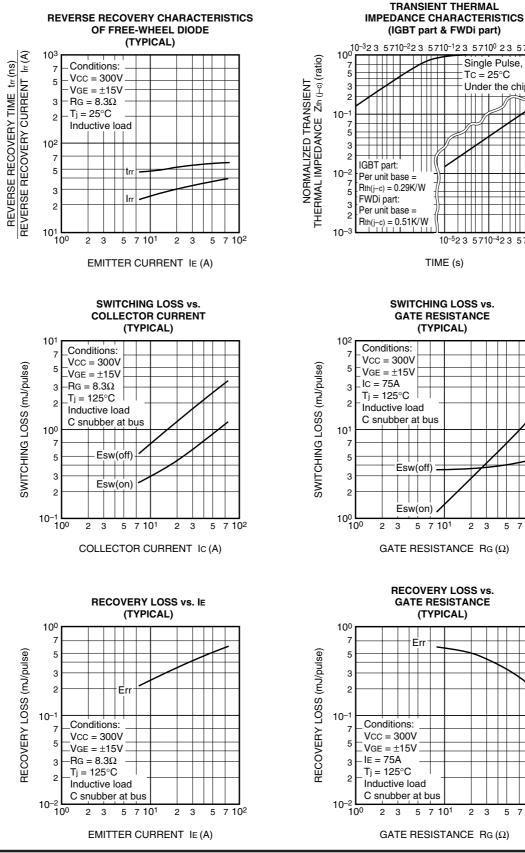
FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)

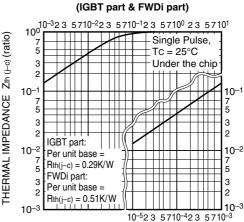


EMITTER-COLLECTOR VOLTAGE VEC (V)



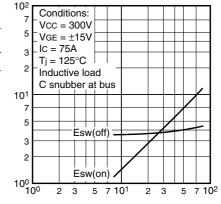
HIGH POWER SWITCHING USE





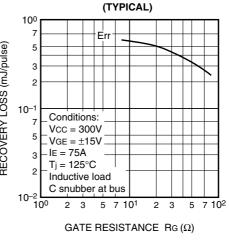
TIME (s)

SWITCHING LOSS vs. GATE RESISTANCE (TYPICAL)



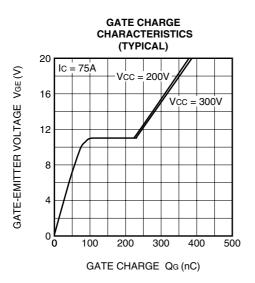
GATE RESISTANCE $RG(\Omega)$

RECOVERY LOSS vs. GATE RESISTANCE





HIGH POWER SWITCHING USE





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