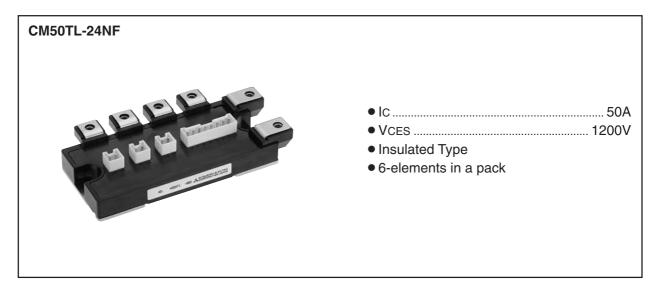
MITSUBISHI IGBT MODULES

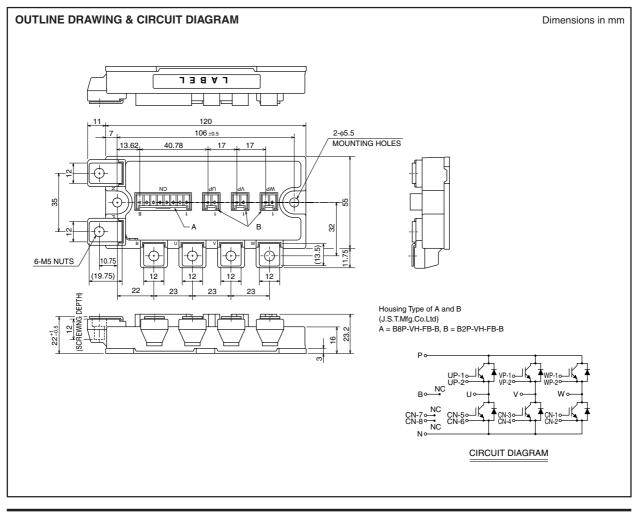
# CM50TL-24NF

HIGH POWER SWITCHING USE



#### APPLICATION

AC drive inverters & Servo controls, etc





#### **HIGH POWER SWITCHING USE**

#### ABSOLUTE MAXIMUM RATINGS (Tj = 25°C, unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit	
VCES	Collector-emitter voltage	G-E Short		1200	V
VGES	Gate-emitter voltage	C-E Short		±20	V
Ic	Collector current	DC, Tc = $94^{\circ}C^{*1}$	50	A	
Ісм	Collector current	Pulse	(Note 2)	100	A
IE (Note 1)	Emitter current			50	A
IEM (Note 1)	Emiller current	Pulse	(Note 2)	100	A
PC (Note 3)	Maximum collector dissipation	Tc = 25°C		390	W
Tj	Junction temperature			-40 ~ +150	°C
Tstg	Storage temperature			-40 ~ +125	°C
Viso	Isolation voltage	Terminals to base plate, f = 60Hz, AC 1	minute	2500	Vrms
_	To you to a two parts	Main terminals M5 screw		2.5 ~ 3.5	N • m
	Torque strength	Mounting M5 screw		2.5 ~ 3.5	N • m
_	Weight	Typical value		350	g

#### ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified)

Symbol	Demonster	Test conditions		Limits			
	Parameter			Min.	Тур.	Max.	Unit
ICES	Collector cutoff current	VCE = VCES, VGE = 0V		—	—	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 5.0mA, VCE = 10V		6	7	8	v
IGES	Gate leakage current	$\pm$ VGE = VGES, VCE = 0V		_	_	0.5	μA
VCE(sat)	Collector-emitter saturation voltage	IC = 50A, VGE = 15V	Tj = 25°C	_	2.1	3.0	- V
			Tj = 125°C	—	2.4	—	
Cies	Input capacitance	VCE = 10V VGE = 0V		_	—	8.5	nF
Coes	Output capacitance			—	—	0.75	nF
Cres	Reverse transfer capacitance			_	_	0.17	nF
QG	Total gate charge	VCC = 600V, IC = 50A, VGE = 15V		_	250	—	nC
td(on)	Turn-on delay time			—	—	100	ns
tr	Turn-on rise time	Vcc = 600V, Ic = 50A VGE = $\pm 15V$ RG = $6.3\Omega$ , Inductive load IE = 50A		_	_	50	ns
td(off)	Turn-off delay time			_	_	300	ns
tf	Turn-off fall time			_	_	350	ns
trr (Note 1)	Reverse recovery time			_	_	100	ns
Qrr (Note 1)	Reverse recovery charge			_	2	_	μC
VEC(Note 1)	Emitter-collector voltage	IE = 50A, VGE = 0V		_	_	3.8	V
Rth(j-c)Q	<b></b>	IGBT part (1/6 module) <sup>*1</sup>		_	_	0.32	K/W
Rth(j-c)R	Thermal resistance	FWDi part (1/6 module) <sup>*1</sup>		_	_	0.43	K/W
Rth(c-f)	Contact thermal resistance	Case to heat sink, Thermal compound Applied (1/6 module)*2		_	0.085	_	K/W
RG	External gate resistance			6.3	_	96	Ω

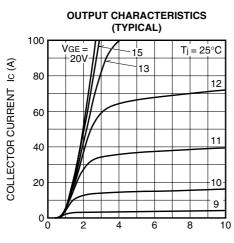
\*1 : Case temperature (Tc) measured point is just under the chips. If you use this value, Rth(f-a) should be measured just under the chips.
 \*2 : Typical value is measured by using thermally conductive grease of λ = 0.9[W/(m • K)].

Note 1. IE, VEC, trr & Qrr represent characteristics of the anti-parallel, emitter-collector free-wheel diode (FWDi).
2. Pulse width and repetition rate should be such that the device junction temperature (Tj) does not exceed Tjmax rating.
3. Junction temperature (Tj) should not increase beyond 150°C.
4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

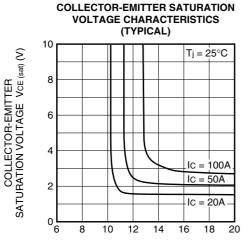


#### **HIGH POWER SWITCHING USE**



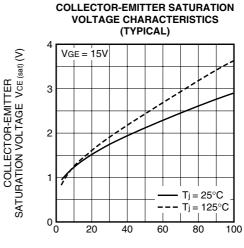


COLLECTOR-EMITTER VOLTAGE VCE (V)



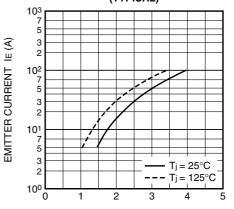
GATE-EMITTER VOLTAGE VGE (V)

CAPACITANCE-VCE **CHARACTERISTICS** (TYPICAL)  $10^{2}$ Ę Coes, Cres (nF) 32 10<u>1</u> į 3 2 CAPACITANCE Cies, 10<u>0</u> į oes 32 10-1 Cres į 3 2 VGE = 0V10-2  $10^{-1}$  2 3 5 7 10<sup>0</sup> 2 3 5 7 10<sup>1</sup> 2 3 5 7 10<sup>2</sup> COLLECTOR-EMITTER VOLTAGE VCE (V)

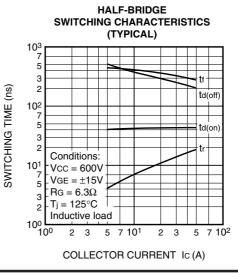


COLLECTOR CURRENT Ic (A)

FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



EMITTER-COLLECTOR VOLTAGE VEC (V)



#### **HIGH POWER SWITCHING USE**

Single Pulse,

Under the chip

10-523 5710-423 5710-3

 $10^{-1}$ 

5

3

2

5

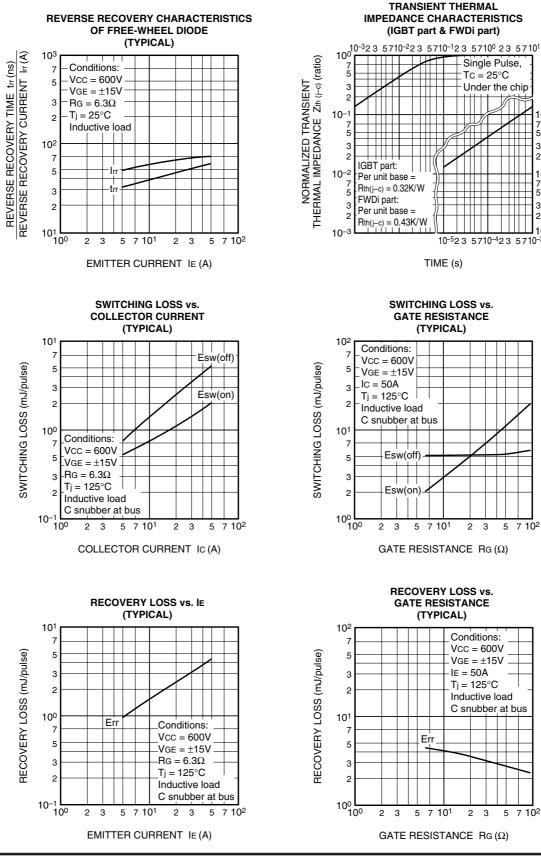
3

2

10-3

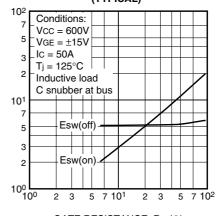
10-2

TC = 25°C



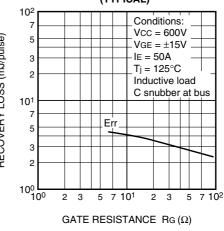
SWITCHING LOSS vs. GATE RESISTANCE (TYPICAL)

TIME (s)



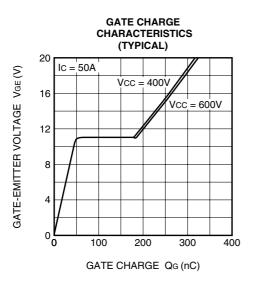
GATE RESISTANCE  $RG(\Omega)$ 

#### **RECOVERY LOSS vs.** GATE RESISTANCE (TYPICAL)





HIGH POWER SWITCHING USE





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