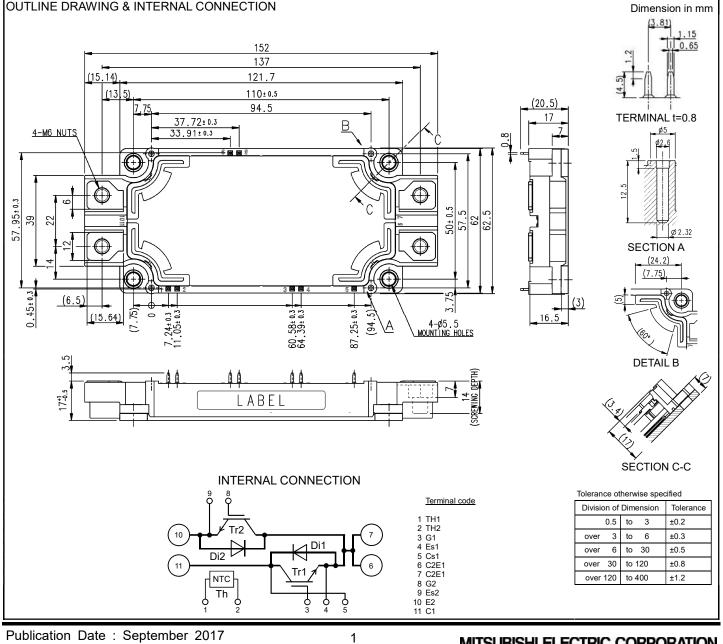


<IGBT Modules>

# CM300DX-24S1

HIGH POWER SWITCHING USE INSULATED TYPE

	Collector current I <sub>C</sub>	300A
	Collector-emitter voltage V <sub>CES</sub>	1 2 0 0 V
	Maximum junction temperature T <sub>jmax</sub>	175°C
	●Flat base Type	
	<ul> <li>Copper base plate (non-plating)</li> </ul>	
	<ul> <li>Tin plating pin terminals</li> </ul>	
	<ul> <li>RoHS Directive compliant</li> </ul>	
dual switch (Half-Bridge)	•UL Recognized under UL1557, File No. E323585	
APPLICATION		
AC Motor Control, Motion/Servo Control, Power	supply, etc.	
OUTLINE DRAWING & INTERNAL CONNECTION		Dimension in mm



MITSUBISHI ELECTRIC CORPORATION

## MAXIMUM RATINGS (Tj=25 °C, unless otherwise specified)

#### **INVERTER PART IGBT/FWD**

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	1200	V
V <sub>GES</sub>	Gate-emitter voltage	C-E short-circuited	± 20	V
lc	Collector ourrent	DC, T <sub>C</sub> =107 °C (Note2, 4)	300	
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	600	
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	1850	W
IE (Note1)		DC (Note2)	300	
I <sub>ERM</sub> (Note1)	Emitter current	Pulse, Repetitive (Note3)	600	A
	-			

#### MODULE

Symbol	Item	Conditions	Rating	Unit
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T <sub>jmax</sub>	Maximum junction temperature	Instantaneous event (overload)	175	°C
T <sub>Cmax</sub>	Maximum case temperature	(Note4)	125	
Tjop	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C
T <sub>stg</sub>	Storage temperature	-	-40 ~ +125	

#### ELECTRICAL CHARACTERISTICS (Tj=25 °C, unless otherwise specified)

#### INVERTER PART IGBT/FWD

Symbol	ltem	Conditions			Limits		Unit
Symbol	item	Conditions		Min.	Тур.	Max.	Unit
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μA
$V_{\text{GE(th)}}$	Gate-emitter threshold voltage	I <sub>C</sub> =30 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
		I <sub>C</sub> =300 A, V <sub>GE</sub> =15 V,	T <sub>j</sub> =25 °C	-	1.80	2.25	
V <sub>CEsat</sub> (Terminal)		Refer to the figure of test circuit	T <sub>j</sub> =125 °C	-	2.00	-	V
(Terminar)	Collector omitter acturation voltage	(Note5)	T <sub>j</sub> =150 °C	-	2.05	-	
.,	Collector-emitter saturation voltage	I <sub>C</sub> =300 A,	T <sub>j</sub> =25 °C	-	1.70	2.15	
V <sub>CEsat</sub>		V <sub>GE</sub> =15 V,	T <sub>j</sub> =125 °C	-	1.90	-	V
(Chip)		(Note5)	T <sub>j</sub> =150 °C	-	1.95	-	
Cies	Input capacitance				-	30	
C <sub>oes</sub>	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	6.0	nF
Cres	Reverse transfer capacitance			-	-	0.5	
$Q_{G}$	Gate charge	V <sub>CC</sub> =600 V, I <sub>C</sub> =300 A, V <sub>GE</sub> =15 V		-	630	-	nC
t <sub>d(on)</sub>	Turn-on delay time	Vcc=600 V, Ic=300 A, VgE=±15 V,		-	-	800	
tr	Rise time			-	-	200	
$t_{d(off)}$	Turn-off delay time			-	-	600	ns
t <sub>f</sub>	Fall time	$R_{\rm G}=0 \Omega$ , inductive load	$R_G=0 \Omega$ , Inductive load		-	300	
A (Al-t-1)		I <sub>E</sub> =300 A, G-E short-circuited,	T <sub>j</sub> =25 °C	-	2.60	3.40	
V <sub>EC</sub> <sup>(Note1)</sup>		Refer to the figure of test circuit	T <sub>j</sub> =125 °C	-	2.16	-	V
(Terminal)		(Note5)	T <sub>j</sub> =150 °C	-	2.10	-	1
	Emitter-collector voltage	I <sub>E</sub> =300 A,	T <sub>j</sub> =25 °C	-	2.50	3.30	
V <sub>EC</sub> <sup>(Note1)</sup>		G-E short-circuited,	T <sub>j</sub> =125 °C	-	2.06	-	V
(Chip)		(Note5)	T <sub>j</sub> =150 °C	-	2.00	-	
t <sub>rr</sub> <sup>(Note1)</sup>	Reverse recovery time	V <sub>CC</sub> =600 V, I <sub>E</sub> =300 A, V <sub>GE</sub> =±15 V,		-	-	300	ns
Qrr (Note1)	Reverse recovery charge	R <sub>G</sub> =0 Ω, Inductive load		-	8.0	-	μC
Eon	Turn-on switching energy per pulse	V <sub>CC</sub> =600 V, I <sub>C</sub> =I <sub>E</sub> =300 A,		-	26.7	-	
E <sub>off</sub>	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, \text{ R}_{G}=0 \Omega, \text{ T}_{j}=150 \text{ °C},$		-	35.7	-	mJ
Err (Note1)	Reverse recovery energy per pulse	Inductive load		-	18.6	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, Tc=25 °C (Note2)		-	-	0.9	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch		-	6.5	-	Ω

## ELECTRICAL CHARACTERISTICS (cont.; $T_j$ =25 °C, unless otherwise specified) NTC THERMISTOR PART

Symbol Item	Conditions		Unit			
	Conditions	Min.	Тур.	Max.	Unit	
R <sub>25</sub>	Zero-power resistance	T <sub>C</sub> =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R <sub>100</sub> =493 Ω, T <sub>C</sub> =100 °C <sup>(Note4)</sup>	-7.3	-	+7.8	%
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note6)	-	3375	-	К
P <sub>25</sub>	Power dissipation	T <sub>C</sub> =25 °C (Note4)	-	-	10	mW

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol Item	Conditions		Unit			
	Conditions	Min.	Тур.	Max.	Unit	
R <sub>th(j-c)Q</sub>	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	81	K/kW
R <sub>th(j-c)D</sub>	th(j-c)D	Junction to case, per Inverter FWD (Note4)	-	-	130	N/KVV
R <sub>th(c-s)</sub> Contact thermal resistance	Case to heat sink, per 1 module,		15	-	K/kW	
	Thermal grease applied (Note4, 7)	-	15		N/KVV	

#### MECHANICAL CHARACTERISTICS

Symbol	Symbol Item	Conditions		Limits			Unit
Symbol		Conditions		Min.	Тур.	Max.	
Mt	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N∙m
m	mass	-		-	350	-	g
d	d Oracina na diatana a	Terminal to terminal		17	-	-	
ds	Creepage distance	Terminal to base plate		18.5	-	-	mm
d	Clearance	Terminal to terminal		10	-	-	
d <sub>a</sub> Clearance	Terminal to base plate		16.3	-	-	mm	
ec	Flatness of base plate	On the centerline X, Y (Note8)		±0	-	+100	μm

\*. This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

2. Junction temperature  $(T_j)$  should not increase beyond  $T_{jmax}$  rating.

3. Pulse width and repetition rate should be such that the device junction temperature  $(T_j)$  dose not exceed  $T_{jmax}$  rating.

4. Case temperature ( $T_c$ ) and heat sink temperature ( $T_s$ ) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise.

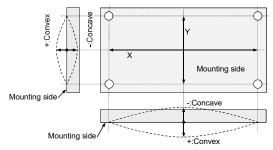
6. B(25/50)=ln(
$$\frac{R_{25}}{R_{50}}$$
)/( $\frac{1}{T_{25}}$ - $\frac{1}{T_{50}}$ )

 $R_{25}\!\!:$  resistance at absolute temperature  $T_{25}$  [K],  $T_{25}\!\!=\!\!25$  [°C] +273.15=298.15 [K]

 $R_{50}\!\!:$  resistance at absolute temperature  $T_{50}$  [K],  $T_{50}\!\!=\!\!50$  [°C] +273.15=323.15 [K]

7. Typical value is measured by using thermally conductive grease of  $\lambda \text{=}0.9$  W/(m·K).

8. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



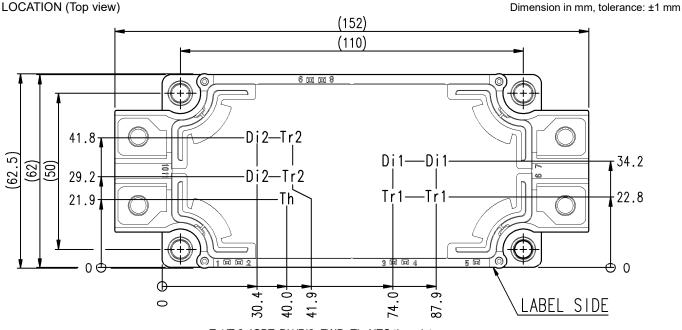
9. Use the following screws when mounting the printed circuit board (PCB) on the standoffs. PCB thickness : t=1.6

Туре	Manufacturer	Size	Tightening torque (N•m)	Recommended tightening method
(1) PT®	EJOT	K25×8	0.55 ± 0.055	
(2) PT®	1	K25×10	0.75 ± 0.075	by handwork (equivalent to 30 rpm
(3) DELTA PT®	1	25×8	0.55 ± 0.055	by mechanical screw driver)
(4) DELTA PT®	1	25×10	0.75 ± 0.075	~ 600 rpm (by mechanical screw driver)
(5) B1 tapping screw	-	φ2 <u>.</u> 6×10	0.75 ± 0.075	]
		φ2.6×12		

#### RECOMMENDED OPERATING CONDITIONS

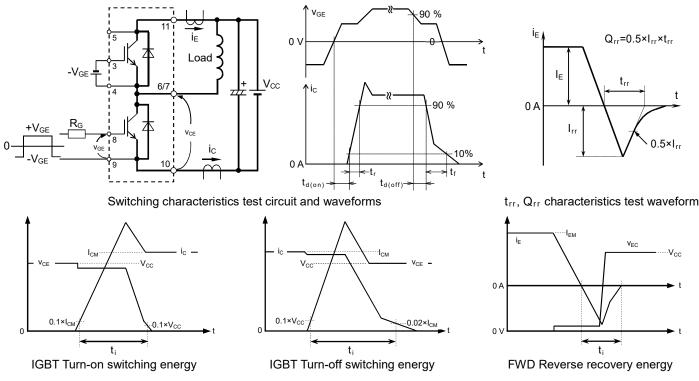
Symbol Item	Itom	Conditions		Limits			
Symbol		Conditions	Min.	Тур.	Max.	Unit	
Vcc	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V	
V <sub>GEon</sub>	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V	
R <sub>G</sub>	External gate resistance	Per switch	0	-	15	Ω	

#### CHIP LOCATION (Top view)



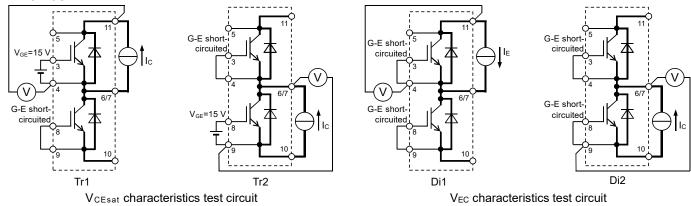
Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor





Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

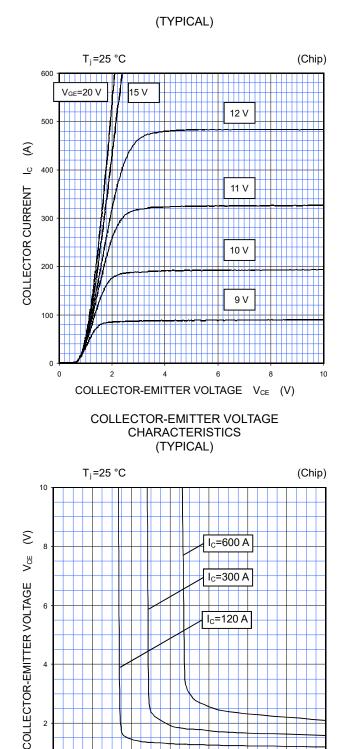
#### **TEST CIRCUIT**

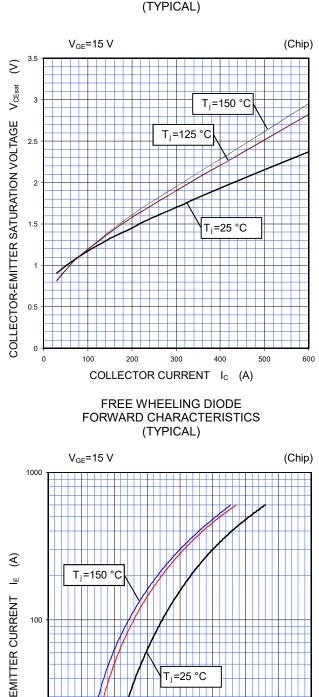


#### PERFORMANCE CURVES

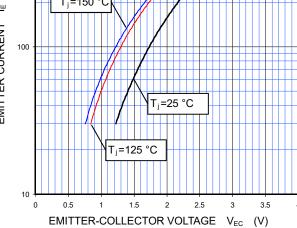
#### **INVERTER PART**

**OUTPUT CHARACTERISTICS** 





COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS



Publication Date : September 2017 CMH-10296-B Ver.1.3

10

12

GATE-EMITTER VOLTAGE VGE (V)

14

16

18

20

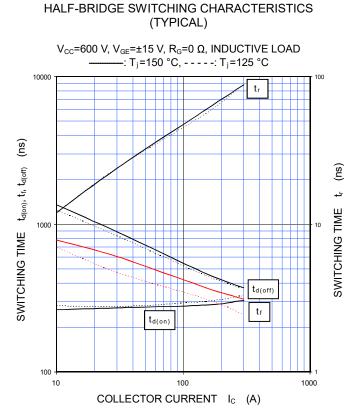
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6

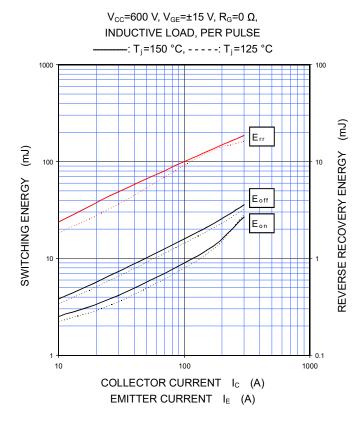
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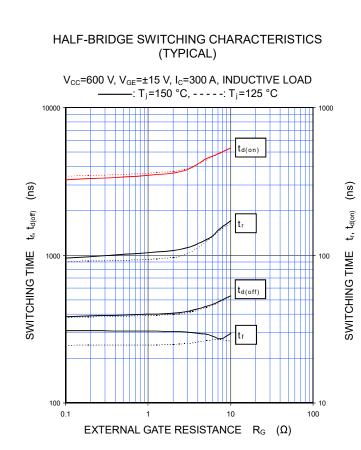
#### PERFORMANCE CURVES

#### **INVERTER PART**

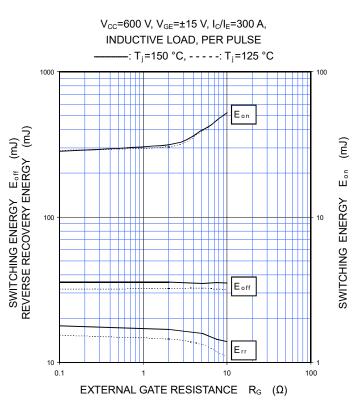








HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

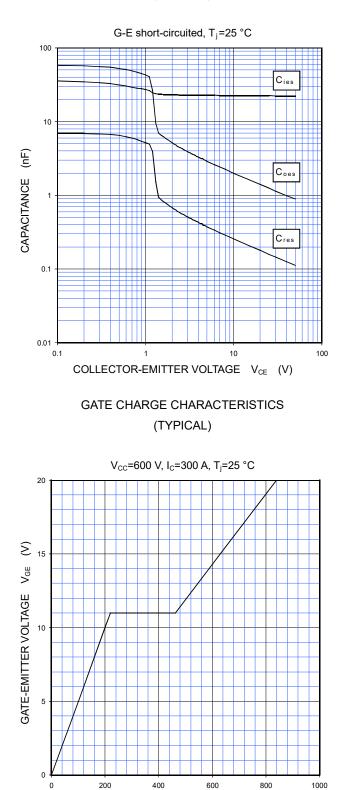


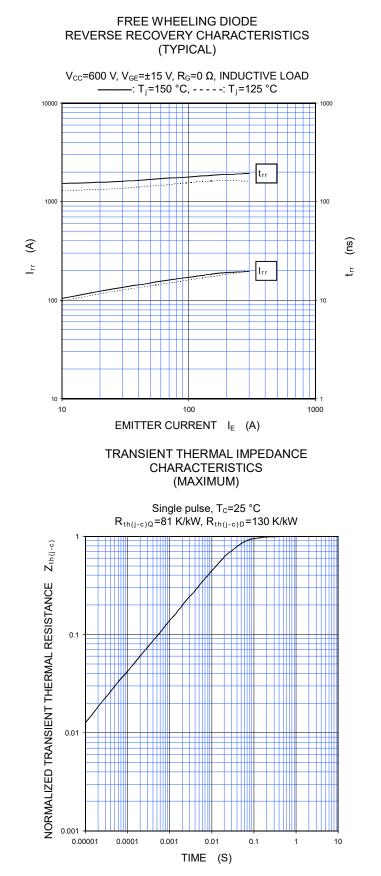
#### PERFORMANCE CURVES

#### **INVERTER PART**

CAPACITANCE CHARACTERISTICS

(TYPICAL)

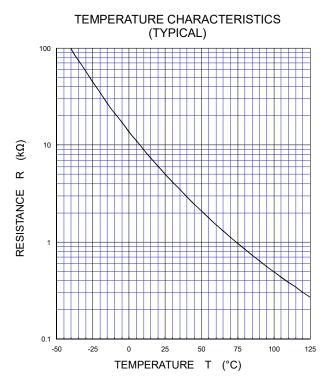




GATE CHARGE Q<sub>G</sub> (nC)

#### PERFORMANCE CURVES

NTC thermistor part



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