

### **SEMITRANS® 3**

### Trench IGBT Modules

### SKM600GAL07E3

### Features\*

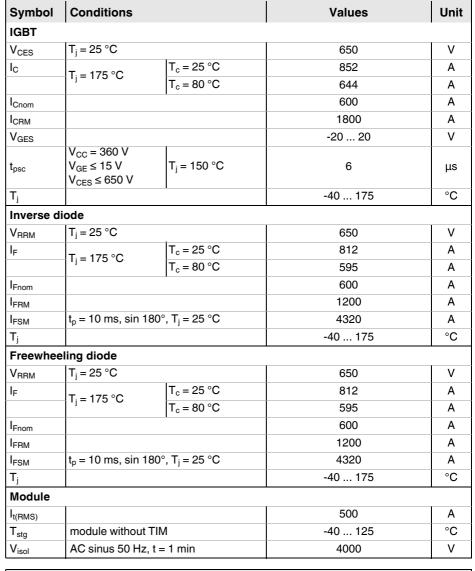
- V<sub>CE(sat)</sub> with positive temperature coefficient
- · High short circuit capability, self limiting to 6 x I<sub>cnom</sub>
- · Fast & soft switching inverse CAL diodes
- Insulated copper baseplate using DCB Technology (Direct Copper Bonding)
- · With integrated gate resistor

### **Typical Applications**

- · Electronic welders
- DC/DC converter
- · Brake chopper
- · Switched reluctance motor

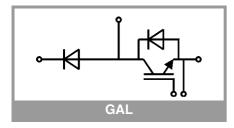
### Remarks

- · Case temperature limited to  $T_c = 125^{\circ}C$  max.
- Recommended T<sub>op</sub> = -40 ... +150°C
- · Product reliability results valid for  $T_i = 150$ °C
- · Use of soft R<sub>G</sub> necessary



**Absolute Maximum Ratings** 

Characte	eristics					
Symbol	Conditions	min.	typ.	max.	Unit	
IGBT						•
V <sub>CE(sat)</sub>	$I_C = 600 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	T <sub>j</sub> = 25 °C		1.45	1.90	V
		T <sub>j</sub> = 150 °C		1.70	2.10	V
V <sub>CE0</sub>	chiplevel	T <sub>j</sub> = 25 °C		0.90	1.00	V
		T <sub>j</sub> = 150 °C		0.82	0.90	V
r <sub>CE</sub>	V <sub>GE</sub> = 15 V chiplevel	T <sub>j</sub> = 25 °C		0.92	1.50	mΩ
		T <sub>j</sub> = 150 °C		1.47	2.00	mΩ
$V_{GE(th)}$	V <sub>GE</sub> =V <sub>CE</sub> , I <sub>C</sub> = 9.6 mA		5.1	5.8	6.4	V
I <sub>CES</sub>	$V_{GE} = 0 \text{ V}, V_{CE} = 650 \text{ V}, T_j = 25 ^{\circ}\text{C}$				0.3	mA
C <sub>ies</sub>	V <sub>CE</sub> = 25 V V <sub>GE</sub> = 0 V	f = 1 MHz		37.0		nF
Coes		f = 1 MHz		2.32		nF
C <sub>res</sub>		f = 1 MHz		1.10		nF
$Q_{G}$	V <sub>GE</sub> = - 8 V+ 15 V			4800		nC
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C			0.5		Ω





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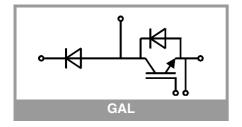
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Characte	ristics					
Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
t <sub>d(on)</sub>	V <sub>CC</sub> = 300 V	T <sub>i</sub> = 150 °C		83		ns
t <sub>r</sub>	$I_{\rm C} = 600  {\rm A}$	T <sub>i</sub> = 150 °C		121		ns
E <sub>on</sub>	$V_{GE} = +15/-7.5 \text{ V}$ $R_{G \text{ on}} = 3 \Omega$	T <sub>j</sub> = 150 °C		20		mJ
t <sub>d(off)</sub>	$R_{G \text{ off}} = 4.3 \Omega$	T <sub>j</sub> = 150 °C		1100		ns
t <sub>f</sub>	di/dt <sub>on</sub> = 4900 A/μs	T <sub>j</sub> = 150 °C		93		ns
E <sub>off</sub>	$\begin{array}{l} \hline \text{di/dt}_{\text{off}} = 6700 \text{ A/}\mu\text{s} \\ \text{dv/dt} = 1330 \text{ V/}\mu\text{s} \\ \text{L}_{\text{s}} = 20 \text{ nH} \end{array}$	T <sub>j</sub> = 150 °C		37		mJ
R <sub>th(j-c)</sub>	per IGBT				0.066	K/W
R <sub>th(c-s)</sub>	per IGBT (λ <sub>grease</sub> =0.81 W/(m*K))			0.033		K/W
R <sub>th(c-s)</sub>	per IGBT, pre-applied phase change material			0.02		K/W
Inverse d	iode					
$V_F = V_{EC}$	I <sub>F</sub> = 600 A	T <sub>j</sub> = 25 °C		1.40	1.76	V
	V <sub>GE</sub> = 0 V chiplevel	T <sub>j</sub> = 150 °C		1.38	1.77	٧
V <sub>F0</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.04	1.24	V
		T <sub>j</sub> = 150 °C		0.85	0.99	V
r <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		0.60	0.88	mΩ
		T <sub>j</sub> = 150 °C		0.89	1.31	mΩ
I <sub>RRM</sub>	I <sub>F</sub> = 600 A	T <sub>j</sub> = 150 °C		390		Α
Q <sub>rr</sub>	di/dt <sub>off</sub> = 4940 A/μs V <sub>GE</sub> = -7.5 V	T <sub>j</sub> = 150 °C		54		μC
$E_{rr}$	V <sub>CC</sub> = 300 V	T <sub>j</sub> = 150 °C		9.1		mJ
R <sub>th(j-c)</sub>	per diode				0.096	K/W
R <sub>th(c-s)</sub>	per diode (λ <sub>grease</sub> =0.81 W/(m*K))			0.038		K/W
R <sub>th(c-s)</sub>	per diode, pre-applied phase change material			0.028		K/W
Freewhee	eling diode					
$V_F = V_{EC}$	I <sub>F</sub> = 600 A	T <sub>j</sub> = 25 °C		1.40	1.76	V
	V <sub>GE</sub> = 0 V chiplevel	T <sub>j</sub> = 150 °C		1.38	1.77	V
V <sub>F0</sub>	chiplevel	T <sub>i</sub> = 25 °C		1.04	1.24	V
		T <sub>i</sub> = 150 °C		0.85	0.99	V
r <sub>F</sub>	chiplevel	T <sub>i</sub> = 25 °C		0.60	0.88	mΩ
		T <sub>j</sub> = 150 °C		0.89	1.31	mΩ
I <sub>RRM</sub>	I <sub>F</sub> = 600 A	T <sub>j</sub> = 150 °C		390		Α
Q <sub>rr</sub>	$di/dt_{off} = 4940 \text{ A/}\mu\text{s}$	T <sub>j</sub> = 150 °C		54		μC
E <sub>rr</sub>	$V_{GE} = -7.5 \text{ V}$ $V_{CC} = 300 \text{ V}$	T <sub>i</sub> = 150 °C		9.1		mJ
R <sub>th(j-c)</sub>	per diode	<u> </u>			0.096	K/W
R <sub>th(c-s)</sub>	per diode per diode (λ <sub>grease</sub> =0.81 W/(m*K))			0.038		K/W
R <sub>th(c-s)</sub>	per diode, pre-appl material		0.028		K/W	





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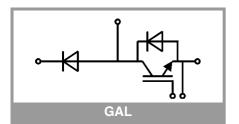
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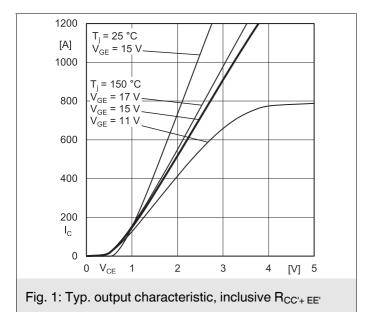
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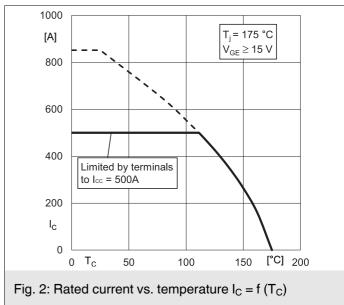
### Remarks

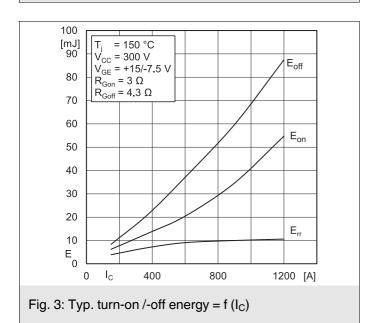
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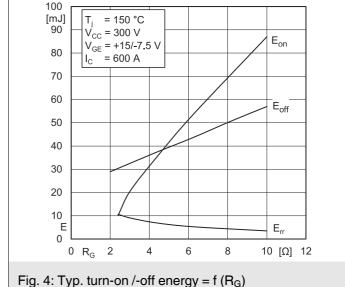
Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Module			•			
L <sub>CE</sub>				15		nΗ
R <sub>CC'+EE'</sub>	measured per switch	T <sub>C</sub> = 25 °C	0.55			mΩ
		T <sub>C</sub> = 125 °C	0.85			mΩ
R <sub>th(c-s)1</sub>	calculated without thermal coupling		0.0177			K/W
R <sub>th(c-s)2</sub>	including thermal coupling, T <sub>s</sub> underneath module (\(\lambda_{\text{grease}}=0.81\) W/(m*K))		0.018			K/W
R <sub>th(c-s)2</sub>	including thermal coupling, T <sub>s</sub> underneath module, pre-applied phase change material			0.012		K/W
Ms	to heat sink M6		3		5	Nm
M <sub>t</sub>		to terminals M6	2.5		5	Nm
	1					Nm
W		,			325	g

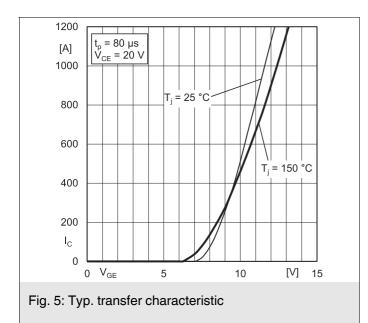












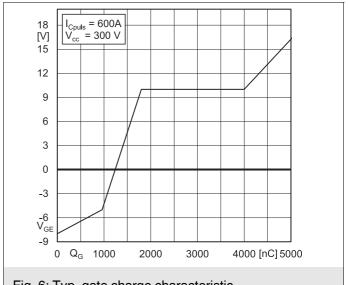
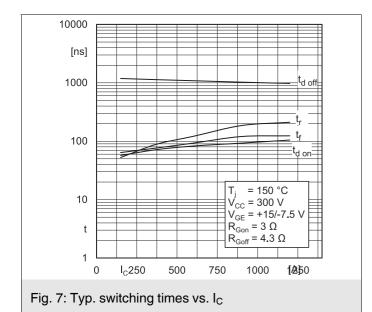
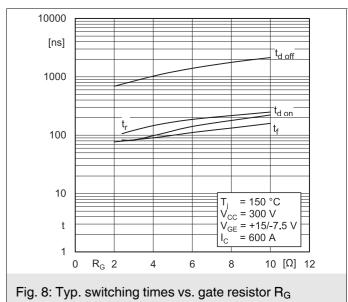
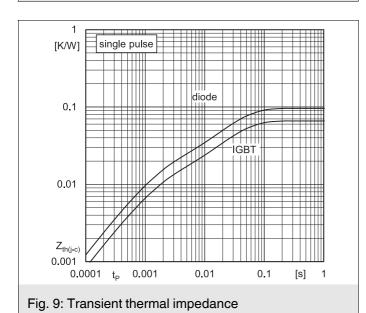
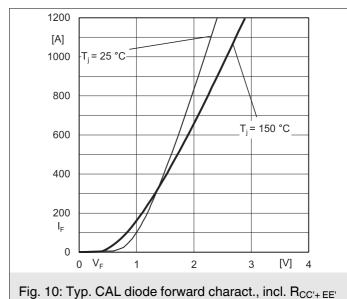


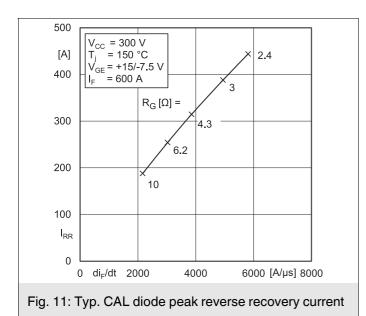
Fig. 6: Typ. gate charge characteristic











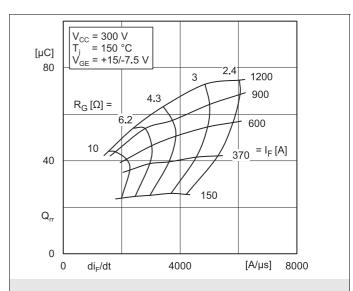
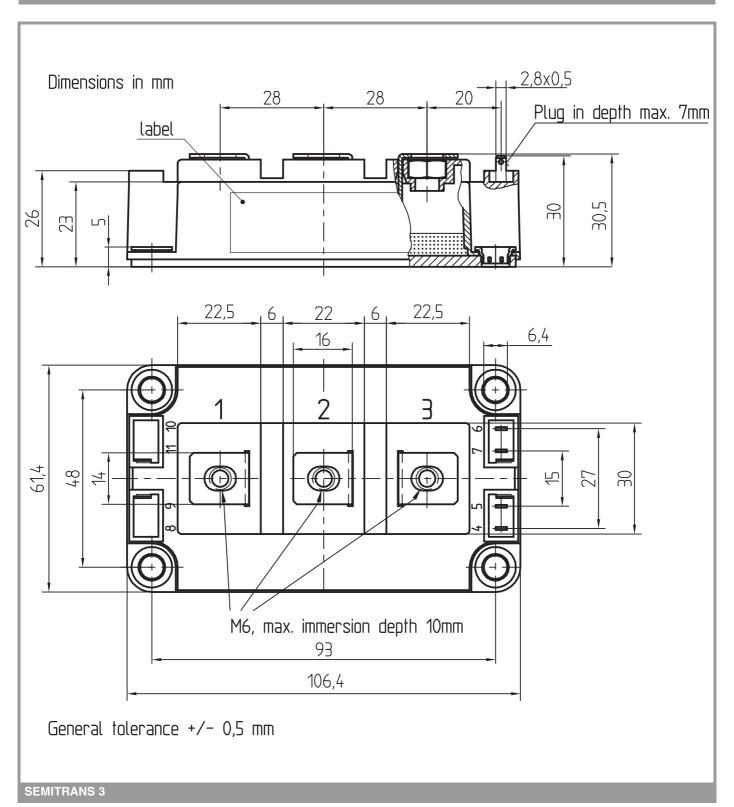
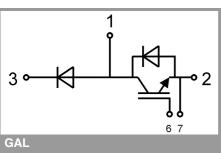


Fig. 12: Typ. CAL diode peak reverse recovery charge





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

#### \*IMPORTANT INFORMATION AND WARNINGS

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