SEMITRANS® 2

Trench IGBT Modules

SKM150GB07E3

Target Data

Features*

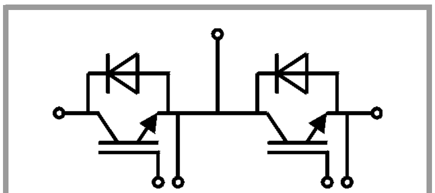
- $V_{CE(sat)}$ with positive temperature coefficient
- High short circuit capability, self limiting to 6 x I_{cnom}
- Fast & soft switching inverse CAL diodes
- Insulated copper baseplate using DCB Technology (Direct Copper Bonding)
- With integrated gate resistor

Typical Applications

- AC inverter drives
- UPS
- Electronic welders
- Wind power
- Public transport

Remarks

- Case temperature limited to T_c = 125°C max.
- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for T_j = 150°C
- Use of soft R_G necessary



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Absolute Maximum Ratings				
Symbol	**Conditions**		**Values**	**Unit**
IGBT				
V_{CES}	T_j = 25 °C		650	V
I_C	T_j = 175 °C	T_c = 25 °C	186	A
		T_c = 80 °C	140	A
I_{Cnom}			150	A
I_{CRM}			450	A
V_{GES}			-20 ... 20	V
t_{psc}	V_{CC} = 360 V $V_{GE} \leq$ 15 V $V_{CES} \leq$ 650 V	T_j = 150 °C	6	µs
T_j			-40 ... 175	°C
Inverse diode				
V_{RRM}	T_j = 25 °C		650	V
I_F	T_j = 175 °C	T_c = 25 °C	203	A
		T_c = 80 °C	149	A
I_{FRM}			300	A
I_{FSM}	t_p = 10 ms, sin 180°, T_j = 25 °C		1200	A
T_j			-40 ... 175	°C
Module				
$I_{t(RMS)}$			200	A
T_{stg}	module without TIM		-40 ... 125	°C
V_{isol}	AC sinus 50 Hz, t = 1 min		4000	V

Characteristics						
Symbol	**Conditions**		**min.**	**typ.**	**max.**	**Unit**
IGBT						
$V_{CE(sat)}$	I_C = 150 A V_{GE} = 15 V chiplevel	T_j = 25 °C		1.46	1.90	V
		T_j = 150 °C		1.71	2.10	V
V_{CE0}	chiplevel	T_j = 25 °C		0.90	1.00	V
		T_j = 150 °C		0.82	0.90	V
r_{CE}	V_{GE} = 15 V chiplevel	T_j = 25 °C		3.7	6.0	mΩ
		T_j = 150 °C		5.9	8.0	mΩ
$V_{GE(th)}$	V_{GE}=V_{CE}, I_C = 2.4 mA		5.1	5.8	6.4	V
I_{CES}	V_{GE} = 0 V, V_{CE} = 650 V, T_j = 25 °C					mA
C_{ies}	V_{CE} = 25 V V_{GE} = 0 V	f = 1 MHz		9.2		nF
C_{oes}		f = 1 MHz		0.58		nF
C_{res}		f = 1 MHz		0.27		nF
Q_G	V_{GE} = - 8 V...+ 15 V			1200		nC
R_{Gint}	T_j = 25 °C			2.0		Ω
$t_{d(on)}$	V_{CC} = 300 V I_C = 150 A V_{GE} = +15/-15 V $R_{G\,on}$ = 1.3 Ω $R_{G\,off}$ = 7.5 Ω	T_j = 150 °C		t.b.d.		ns
t_r		T_j = 150 °C		t.b.d.		ns
E_{on}		T_j = 150 °C		4.7		mJ
$t_{d(off)}$		T_j = 150 °C		t.b.d.		ns
t_f		T_j = 150 °C		t.b.d.		ns
E_{off}		T_j = 150 °C		6.2		mJ
$R_{th(j-c)}$	per IGBT				0.33	K/W
$R_{th(c-s)}$	per IGBT (λ_{grease}=0.81 W/(m*K))			0.064		K/W
$R_{th(c-s)}$	per IGBT, pre-applied phase change material			0.054		K/W

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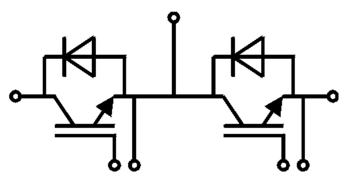

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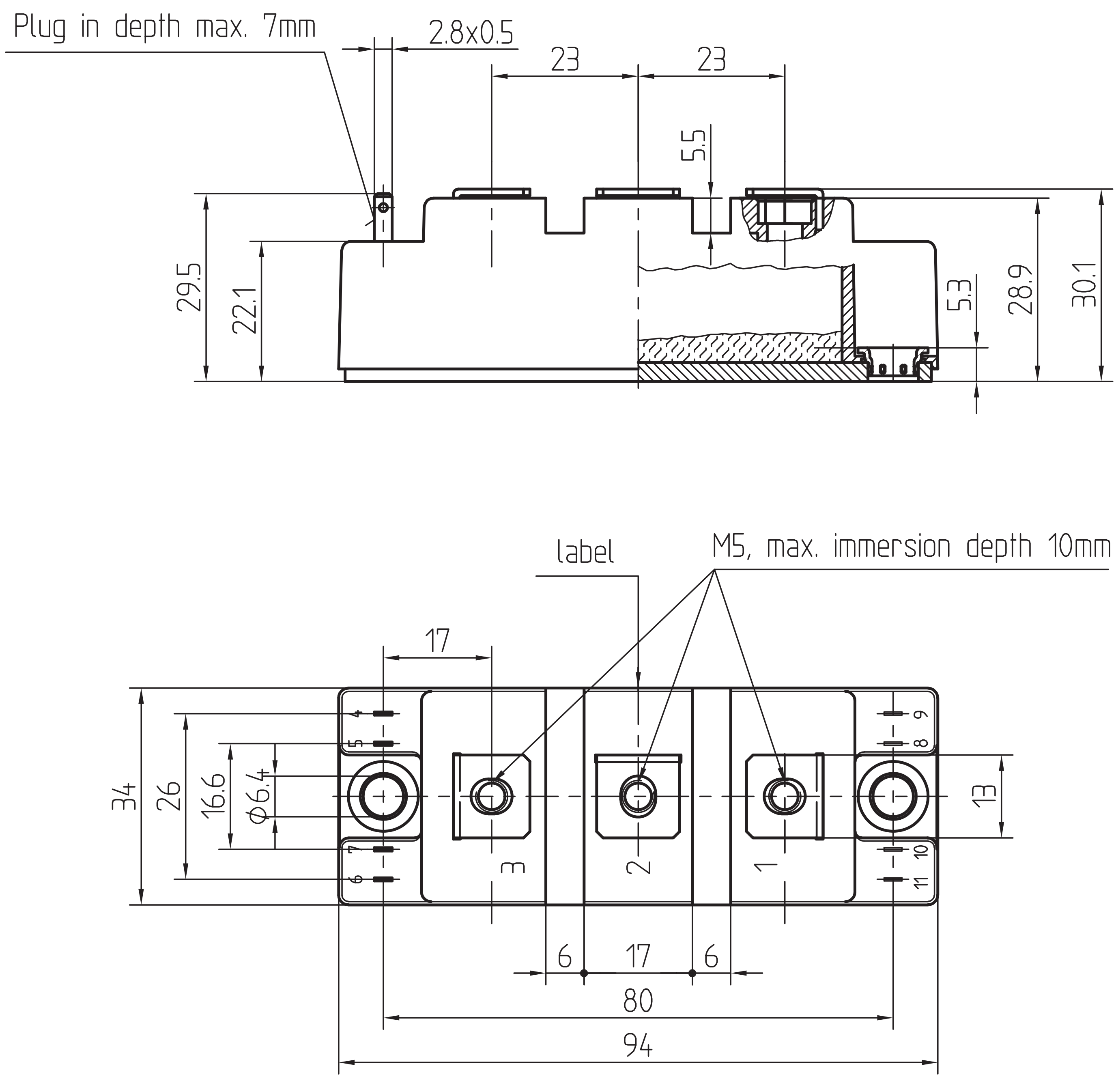
Characteristics						
Symbol	**Conditions**		**min.**	**typ.**	**max.**	**Unit**
Inverse diode						
$V_F = V_{EC}$	I_F = 150 A V_{GE} = 0 V chiplevel	T_j = 25 °C		1.40	1.76	V
		T_j = 150 °C		1.39	1.77	V
V_{F0}	chiplevel	T_j = 25 °C		1.04	1.24	V
		T_j = 150 °C		0.85	0.99	V
r_F	chiplevel	T_j = 25 °C		2.4	3.5	mΩ
		T_j = 150 °C		3.6	5.2	mΩ
I_{RRM}	I_F = 150 A	T_j = 150 °C		t.b.d.		A
Q_{rr}	V_{GE} = -7.5 V V_{CC} = 300 V	T_j = 150 °C		t.b.d.		µC
E_{rr}		T_j = 150 °C		2.9		mJ
$R_{th(j-c)}$	per diode				0.375	K/W
$R_{th(c-s)}$	per diode (λ_{grease}=0.81 W/(m*K))			t.b.d.		K/W
$R_{th(c-s)}$	per diode, pre-applied phase change material			t.b.d.		K/W
Module						
L_{CE}				30		nH
$R_{CC'+EE'}$	measured per switch	T_C = 25 °C		0.65		mΩ
		T_C = 125 °C		1.09		mΩ
$R_{th(c-s)1}$	calculated without thermal coupling			t.b.d.		K/W
$R_{th(c-s)2}$	including thermal coupling, T_s underneath module (λ_{grease}=0.81 W/(m*K))			t.b.d.		K/W
$R_{th(c-s)2}$	including thermal coupling, T_s underneath module, pre-applied phase change material			-		K/W
M_s	to heat sink M6		3		5	Nm
M_t		to terminals M5	2.5		5	Nm
						Nm
w					160	g

Dimensions in mm



General tolerance +/- 0.5 mm

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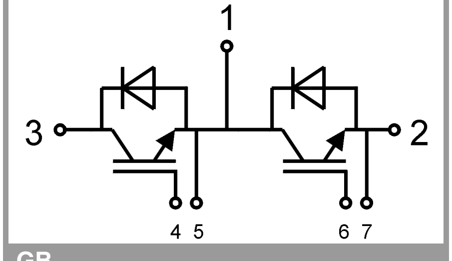

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This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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